

6 5 4 3 2 1

APPROVED: _____
 DATE: _____ TITLE: _____

Rev	AWO #	Description	Date	Appr
-		RELEASED	7/5/18	

RECOMMENDED PCB LAYOUT

SPECIFICATIONS:

Material:
 Housing: PA66, rated UL 94V-0, Color: Black
 Insert: PA46, rated UL 94V-0, Color: Black
 Contacts: Copper Alloy
 Shield: Brass, Nickel plated

Plating:
 Round contact
 Gold over Nickel underplate overall
 Flat contact
 Gold over Nickel underplate on contact area,
 Tin over Nickel underplate on solder tails

Electrical:
A Voltage rating: 150 VAC
 Contact Resistance: 20 mohms max
 Insulation resistance: 500 Mohms min
 Dielectric withstanding voltage: 1500 VAC or 2250 VDC
 Temperature Rating:
 Operating temperature: -40°C to +85°C
 Environmental:
 Lead free, RoHS compliant.

UNLESS OTHERWISE SPECIFIED
 ALL DIMENSIONS ARE INCHES [MM]
 TOLERANCES: EXCEPT AS NOTED
 INCHES HOLES

.X	±.10	∅X	±.10
.XX	±.020	∅XX	±.015
.XXX	±.015	∅XXX	±.010

APPROVALS		DATE
DRAWN	AY	7/5/18
CHECKED	RC	7/5/18
APPROVED		

TITLE: MODULAR TELEPHONE JACK, 1 PORT, 8P8C
 W/ LED1 GREEN, LED2 YELLOW, STAR PEG

SIZE	PART NO.	REV.
X	MTJ-88AX1-FSN-STP-LH-M5-WOT	-
REF: S00005C	SCALE: NTS	SHEET 1 OF 2

6 5 4 3 2 1

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SEE SHT 1 OF 2 FOR REVISION

Circuit Schematic:

CAP 1000pF 2KV

75 OHM

75 OHM

75 OHM

75 OHM

1CT:1CT

1CT:1CT

J8

J7

J5

J4

J6

J3

J2

J1

P8

P6

P5

P4

P3

P2

P1

P9

P10

P11

P12

Pin	Yellow		Green
P9	+	P11	+
P10	-	P12	-

Electronical Specifications:

- Turn Ratio @100KHz: (P1~P3):(J1~J2) = 1:1±5%
(P4~P6):(J3~J6) = 1:1±5%
- Primary Inductance: 350uH MIN @100KHZ,0.1V 8mA DC BIAS
- DC Resistance:(J1~J2):(J3~J6) 1.2 OHMS MAX
- Insertion Loss: 1-100MHz -1.2dB MAX
- Return Loss: 1-30MHz -16dB MIN
30-60MHz -12dB MIN
60-80MHz -10dB MIN
- CROSS TALK: 1-100MHz -30dB MIN
- COMMON TO COMMON MODE ATTENUATION: 1-100MHz -30dB MIN
- Isolation: PHY Side to Line Side: 1500VAC or 2250VDC

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REF: S00005C SCALE: NTS SHEET 2 OF 2

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