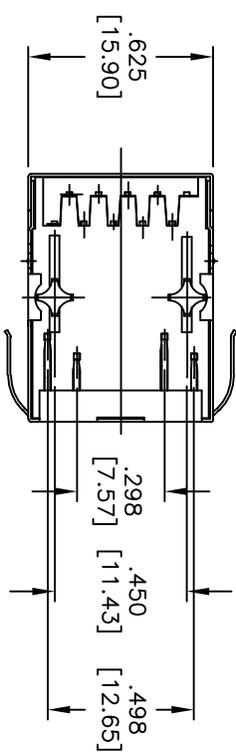
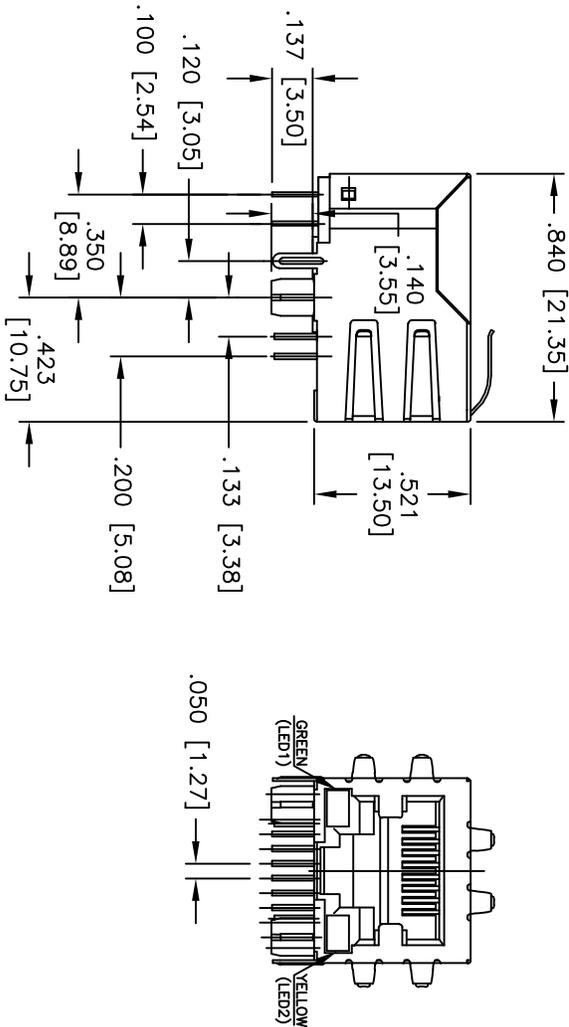


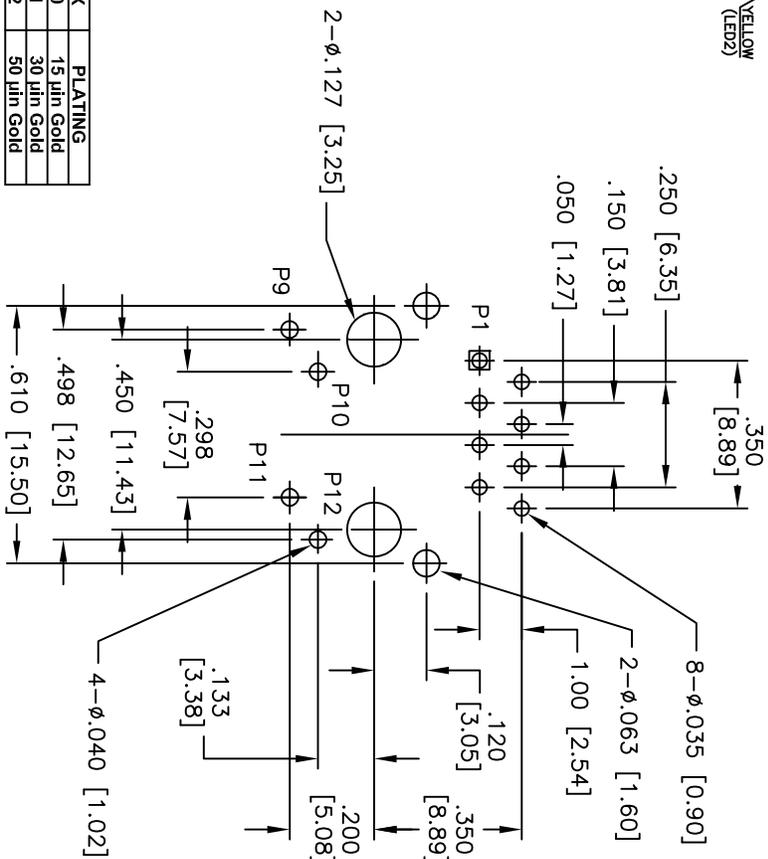
APPROVED: _____
 DATE: _____ TITLE: _____

Rev	AWO #	Description	Date	Appr
-		RELEASED	9/30/10	
A		REVISED SPEC.	1/7/11	
B		REVISED SPEC.	5/5/11	



X	PLATING
0	15 µin Gold
1	30 µin Gold
2	50 µin Gold

PC BOARD LAYOUT



2-φ.127 [3.25]

2-φ.063 [1.60]

1.00 [2.54]

8-φ.035 [0.90]

4-φ.040 [1.02]

1.20 [3.05]

.350 [8.89]

.200 [5.08]

.133 [3.38]

.298 [7.57]

.450 [11.43]

.498 [12.65]

.610 [15.50]

P9

P10

P11

P12

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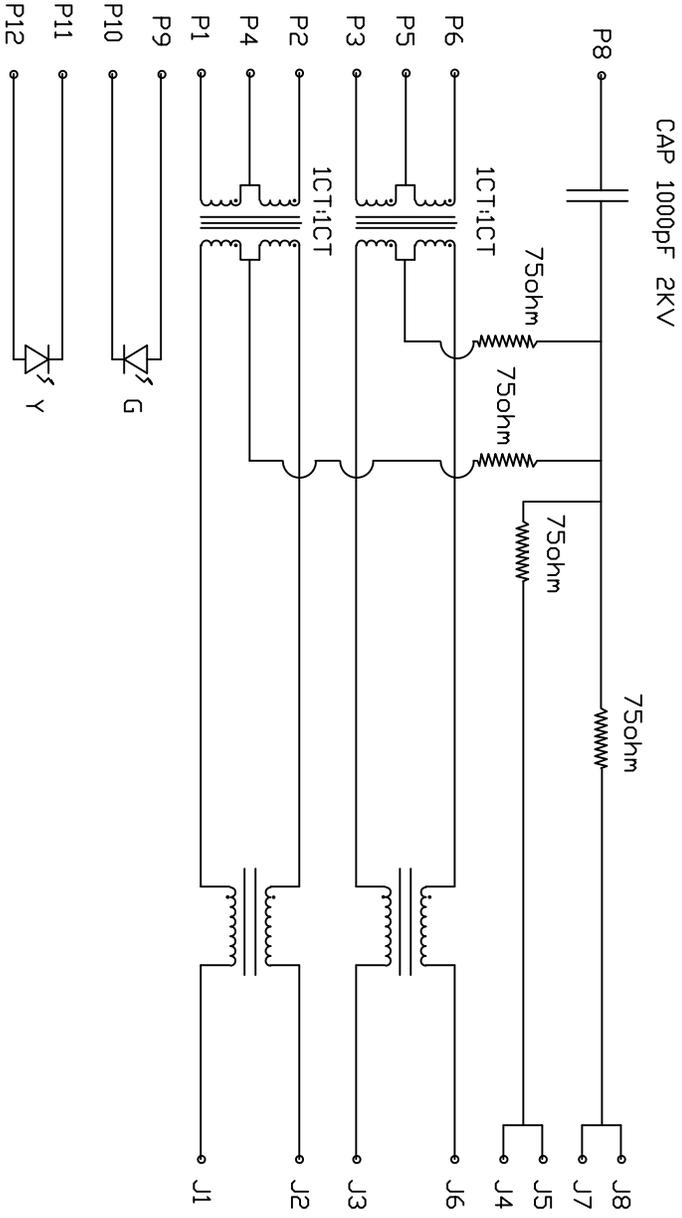
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Rev	AWO #	Description	Date	Appr
		SEE SHT 1 OF 2 FOR REVISION		

APPROVED: _____

DATE: _____ TITLE: _____



ELECTRICAL SPECIFICATIONS:

- Turns Ratio: (P1 ~ P2) : (J1 ~ J2) 1:1 ±3%
(P3 ~ P6) : (J3 ~ J6) 1:1 ±3%
- Inductance: (P1 ~ P2) 350uH min. @ 0.1V, 100KHZ, 8mA DC Bias
(P3 ~ P6) 350uH min. @ 0.1V, 100KHZ, 8mA DC Bias
- Leakage Inductance: P1 ~ P2 (with J1 and J2 Short): 0.35uH Max. @ 1 MHZ
P3 ~ P6 (with J3 and J6 Short): 0.35uH Max. @ 1 MHZ
- Interwinding Capacitance: (P1 ~ P2) : (J1 ~ J2): 35pF max. @ 1 MHZ
(P3 ~ P6) : (J3 ~ J6): 35pF max @ 1mhz
- DC Resistance: (J1 ~ J2):(J3 ~ J6) : 1.2 Ohms Max.
- Return Loss: 1MHz to 30MHz: -18dB min
30MHz to 60MHz: -(18-20log(f/30))dB MIN (WHERE f IS IN MHZ)
60MHz to 80MHz: -12dB min
- Dielectric Withstanding: (P1~P2) : (J1~J2) :1500VAC or 2250VDC
(P3~P6) : (J3~J6) :1500VAC or 2250VDC
- Insertion Loss: 1MHZ to 100MHZ: -35dB Typ.
30MHz to Common mode Attenuation 30MHz to 100MHz: -30dB Typ.

ADAM TECH

909 Rahway Avenue, Union, NJ 07083
Phone: 908-687-5000 Fax: 908-687-5710

MODULAR JACK GANGED, 1 PORT, 8P8C
W/LED1 GREEN, LED2 YELLOW

UNLESS OTHERWISE SPECIFIED ALL DIMENSIONS ARE INCHES [MM]		TOLERANCES: EXCEPT AS NOTED	
INCHES		HOLES	
.X	± .10	ØXX	± .10
.XX	± .020	ØXXX	± .015
.XXX	± .015	ØXXX	± .010
APPROVALS		DATE	
DRAWN	AY		9/30/10
CHECKED	VW		9/30/10
APPROVED			
TITLE		SIZE	PART NO.
MODULAR JACK GANGED, 1 PORT, 8P8C		X	MTJ-88TX1-FSD-PG-D1LH-M121
W/LED1 GREEN, LED2 YELLOW		REF:	S00003C
		SCALE:	NTS
		SHEET:	2 OF 2

6

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4

3

2

1