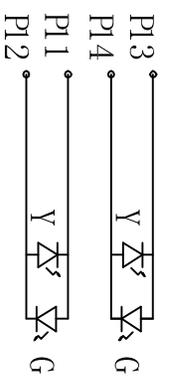
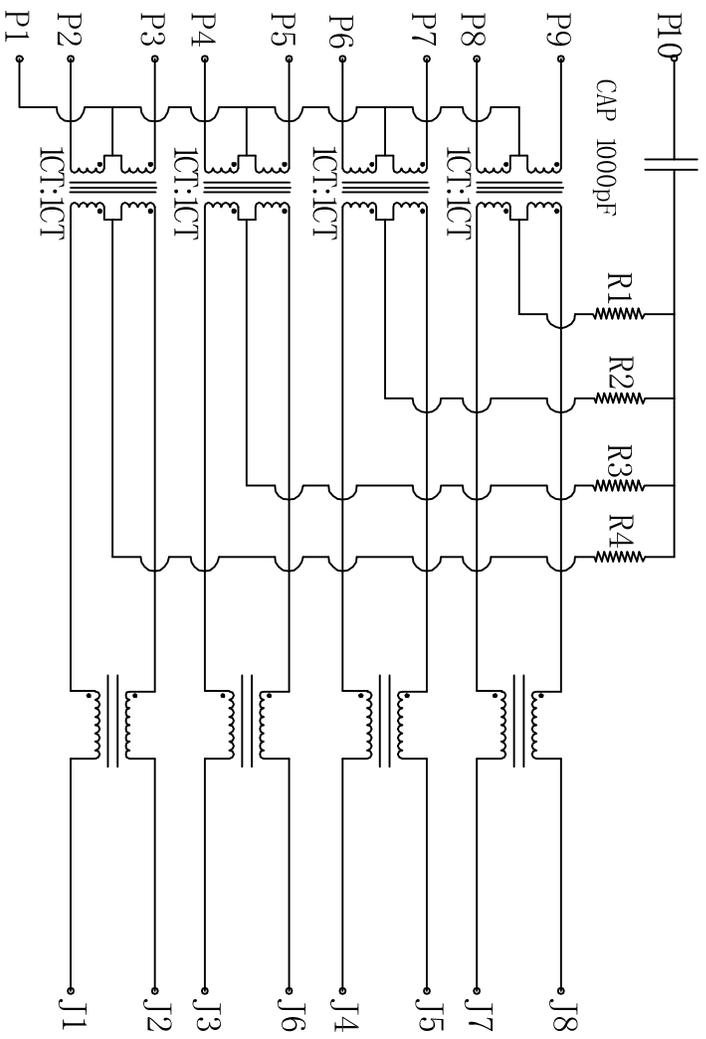




Rev	AWO #	Description	Date	Appr
		SEE SHT 1 OF 2 FOR REVISION		

APPROVED: \_\_\_\_\_

DATE: \_\_\_\_\_ TITLE: \_\_\_\_\_



**ELECTRICAL SPECIFICATIONS**

- Turns Ratio: (P1 ~ P2) : (J1 ~ J2) 1:1 ±3%  
 (P3 ~ P4) : (J3 ~ J6) 1:1 ±3%  
 (P7 ~ P8) : (J4 ~ J5) 1:1 ±3%  
 (P9 ~ P10) : (J7 ~ J8) 1:1 ±3%
- Inductance: (P1 ~ P2) 350uH min. @ 0.1V, 100KHZ, 8mA DC Bias  
 (P3 ~ P4) 350uH min. @ 0.1V, 100KHZ, 8mA DC Bias  
 (P7 ~ P8) 350uH min. @ 0.1V, 100KHZ, 8mA DC Bias  
 (P9 ~ P10) 350uH min. @ 0.1V, 100KHZ, 8mA DC Bias
- Leakage Inductance: P1 ~ P2 (with J1 and J2 Short): 0.4uH Max. @ 1 MHz  
 P3 ~ P4 (with J3 and J6 Short): 0.4uH Max. @ 1 MHz  
 P7 ~ P8 (with J4 and J5 Short): 0.4uH Max. @ 1 MHz  
 P9 ~ P10 (with J7 and J8 Short): 0.4uH Max. @ 1 MHz
- Interwinding Capacitor: (P1 ~ P2) : (J1 ~ J2): 90pF max. @ 1MHz  
 (P3 ~ P4) : (J3 ~ J6): 90pF max @ 1mhz  
 (P7 ~ P8) : (J4 ~ J5): 90pF max @ 1mhz  
 (P9 ~ P10) : (J7 ~ J8): 90pF max @ 1mhz
- DC Resistance: (J1 ~ J2) : (J3 ~ J6) : (J4 ~ J5) : (J7 ~ J8): 1.2 Ohms Max.
- Return Loss: 1MHz to 30MHz: -18dB max / 30MHz to 80MHz: -12dB max
- Dielectric Withstanding: (P1~P2) : (J1~J2): (P3~P4) : (J3~J6) :1500V AC  
 (P7~P8) : (J4~J5): (P9~P10) : (J7~J8) :1500V AC
- Insertion Loss: RS=RL=100OHM : 100KHZ TO 80MHZ : -1.2dB Min.
- Cross Talk: 1MHZ to 100MHZ: 35dB Typ.
- Common to Common mode Attenuation 30MHZ to 100MHZ: 30dB Typ.

UNLESS OTHERWISE SPECIFIED  
 ALL DIMENSIONS ARE INCHES [MM]  
 TOLERANCES: EXCEPT AS NOTED  
 INCHES: .X ±.10 Ø.X ±.10  
 .XX ±.020 Ø.XXX ±.015  
 .XXX ±.015 Ø.XXX ±.010

**ADAM TECH**  
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DRAWN	DY	DATE	5/07/10
CHECKED			
APPROVED			
TITLE	MODULAR JACK GANGED, 1 PORT, 8P8C		
W/ LED1 YEL/GRN, LED2 YEL/GRN			
SIZE	PART NO.	REV.	
X	MTJ-88TX1-FSD-PG-LP-M97		
REF:	S00035C	SCALE:	NTS
		SHEET	2 OF 2