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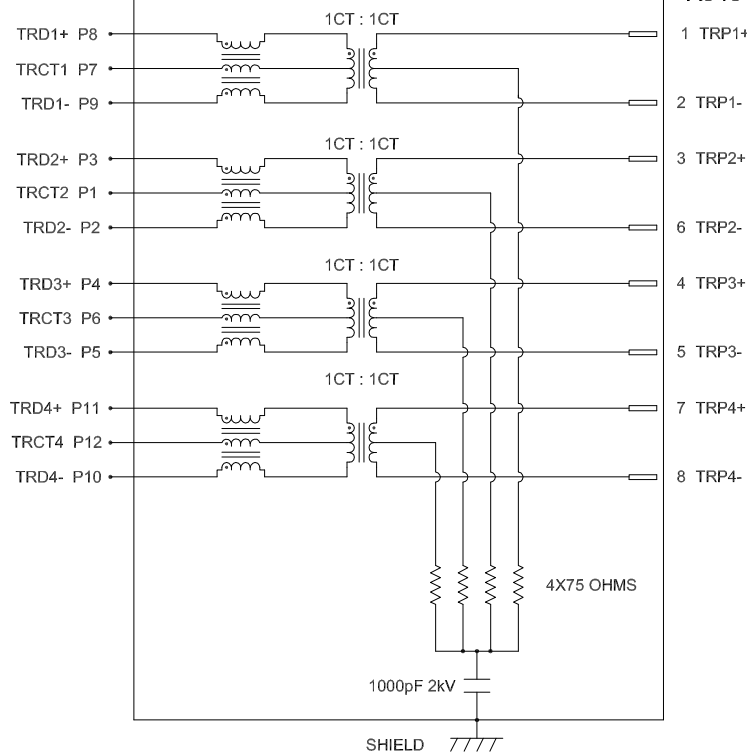
APPROVED: _____

DATE: _____ TITLE: _____

Rev AWO # Description Date Appr

SEE SHT 1 OF 2 FOR REVISION

PHY SIDE



ELECTRICAL SPECIFICATIONS :

PARAMETER	SPECIFICATIONS		
URNS RATIO	1.00 ± 2%		
POLARITY	PER SCHEMATIC		
INSERTION LOSS (DATA CHANNELS)	100 KHz	1-400 MHz	
	-3.0 dB MAX	-2.0 dB MAX (-1.4dB TYP)	
RETURN LOSS (Z OUT=100 OHM ±1%)	1-40 MHz	40 MHz-400 MHz	400 MHz-500 MHz
	-16 dB MIN	-16+10LOG(f/40)dB	-6+30LOG(f/400)dB
INDUCTANCE (OCL)	120 uH MIN @ 100 KHz, 100 mV		
CROSSTALK, ADJACENT CHANNELS	1-100 MHz	100-500 MHz	
	-28 dB MIN	-19 dB MIN	
COMMON MODE REJECTION RATIO	1-40 MHz	40-100 MHz	100-500 MHz
	-30 dB MIN	-28 dB MIN	-26 dB MIN
COMMON TO DIFFERENTIAL MODE REJECTION (CDMR)	1-250 MHz	250-500 MHz	
	-30 dB MIN	-22 dB MIN	
HIPOT-OUTPUT ISOLATION	2250 VDC MIN @ 60 SECONDS		

(10G BASE-T)

UNLESS OTHERWISE SPECIFIED
ALL DIMENSIONS ARE INCHES [MM]
TOLERANCES: EXCEPT AS NOTED
INCHES HOLES
.X ± .10 Ø.X ± .10
.XX ± .020 Ø.XX ± .015
.XXX ± .015 Ø.XXX ± .010



TITLE

MODULAR JACK 10G BASE-T, 1 PORT, 8P8C

APPROVALS		DATE
DRAWN	AY	11/12/18
CHECKED	NL	11/12/18
APPROVED		

SIZE	PART NO.	REV.
X	MTJ-88SUX1-FS-M245C-HT-WOT	B
REF:	S00005C	SCALE: NTS
SHEET 2 OF 2		

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